FPGA workshop 2007

A digital design engineering course

The workshop will take place during the c3a meetings in the rooms of the ACF

http://c3a.de

http://www.augusta.de

03.05 - 21.06.2007

Part I: FPGA Basics

1. 03/05/07
   Introduction to FPGAs and hardware description languages for digital circuit designs
   (Hunz, Till, Frank)

2. 10/05/07
   Tutorial: Introduction to the Xilinx ISE WebPACK
   (Hunz, Till, Frank)

3. 17/05/07
   Tutorial: Sniffing the I2C bus
   (Hunz)

4. 24/05/07
   Design of homebrewn programmable logic development boards
   (Till, Frank)

Part II: Advanced FPGA designs

1. 31/05/07:
   Tutorial: FPGA based logic analyzer
   (Till)
2. 07/06/07
   Detecting serial buses
   (Hunz, Till)
3. 14/06/07
   Proposal: Implementing security algorithms
4. 21/06/07
   Proposal: Designing and implementing the DSP algorithms

The second part is subject to change or might even get rescheduled. This depends on the speakers' initial skill adaption training process.